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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/642,722	08/18/2003	Paul Leventis	ALTRP183/A848	4392
51501	7590	11/22/2005	EXAMINER	
BEYER WEAVER & THOMAS, LLP			TAN, VIBOL	
ATTN: ALTERA			ART UNIT	
P.O. BOX 70250			PAPER NUMBER	
OAKLAND, CA 94612-0250			2819	

DATE MAILED: 11/22/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

AK

Office Action Summary	Application No. 10/642,722	Applicant(s) LEVENTIS ET AL.	
	Examiner Vibol Tan	Art Unit 2819	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 August 2003.
2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-36 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.
5) ☐ Claim(s) _____ is/are allowed.
6) ☒ Claim(s) 1-5, 10-24 and 29-36 is/are rejected.
7) ☒ Claim(s) 6-9 and 25-28 is/are objected to.
8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-5, 10-24 and 29-36 are rejected under 35 U.S.C. 102(b) as being anticipated by Bauer et al. (U. S. PAT. 5,907,248).

In claim 1, Bauer et al. teaches all claimed features in Figs. 1-3, a programmable logic device comprising: circuit regions (tiles) including region input multiplexers (IMUXs) to select signals for routing to the circuit regions (tiles); routing resources (routing lines shown in Fig. 3) including routing resources (routing lines coupled to IMUXs) coupled to the region input multiplexers and including a first routing line (line 47 of 6VN) coupled to route signals between the circuit regions (tile 1 and tile 2), the first routing line (line 47 of 6VN via interconnect couples to output of OMUX) including a first driver input multiplexer ("DIM") (OMUX) to select signals for driving the first routing line; and one or more connection lines (lines 430 connecting output of IMUX to OMUX) coupling respective outputs of one or more of the region input multiplexers to the first DIM.

In claim 2, Bauer et al. further teaches the programmable logic device of claim 1 wherein the first routing line (the line 47 of 6VN) is a longer line than at least some of the other routing resources (longer than 6V).

In claim 3, Bauer et al. further teaches the programmable logic device of claim 1 wherein the connection lines are coupled to the region input multiplexer outputs through programmable connections (PIPs).

In claim 4, Bauer et al. further teaches the programmable logic device of claim 1 wherein the circuit regions (tiles) include logic array blocks (CLEs).

In claim 5, Bauer et al. further teaches the programmable logic device of claim 1 wherein the circuit regions include dedicated blocks (CLEs allocated as dedicated logic elements).

In claim 10, the programmable logic device of Bauer in claim 1 can be considered as a data processing system because it processes data.

Claims 11-15 correspond to detailed circuitry already discussed similarly with regard to claims 1-5.

Claim 16 corresponds to detailed circuitry already discussed similarly with regard to claim 1.

Claims 17-24 and 29 correspond to detailed circuitry already discussed similarly with regard to claims 1-5 and 10.

Claims 30-33 correspond to detailed circuitry already discussed similarly with regard to claims 1-5.

Claims 34-36 correspond to detailed circuitry already discussed similarly with regard to claims 1-5.

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3. Claims 6-9 and 25-28 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vibol Tan whose telephone number is (571) 272-1811. The examiner can normally be reached on Monday-Friday (7:00 AM-4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on (571) 272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



VIBOL TAN
PRIMARY EXAMINER